

METHOD, SYSTEM, AND PROGRAM  
FOR TESTING A BUS INTERFACE

ABSTRACT

5        Provided are a method, system, and program for performing initialization  
operations in a system including a bus, bus interface and at least one bus device  
communicating on the bus. The bus interface includes memory capable of being  
accessed over the bus by the at least one bus device. All bus devices capable of  
communicating on the bus are detected and each detected bus device and bus  
interface is configured with base addresses that enable transmission of Input/Output  
10    (I/O) requests over the bus to the memory in the bus interface and memory in any bus  
device including memory accessible over the bus. Testing is performed on the base  
addresses of the memory in each bus device including memory accessible over the  
bus by issuing I/O requests to the base addresses of the memory in each bus device.  
Memory in the bus interface is tested by issuing I/O requests to the base addresses of  
15    the memory in the bus interface over the bus.

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